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If there are any questions regarding this matter, please contact me at the telephone number provided below.

Respectfully submitted,



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5957-05500
PATENT

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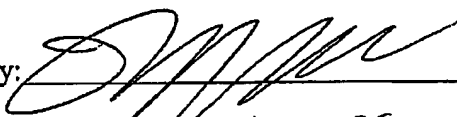
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<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Page 1 of 7</u> <u>Title / Inventors</u> <u>(US Patent)</u>
699,600	US	14/05/1991	Method for manufacturing an active matrix display screen with storage capacitors / Morin, LoComelle
264,199	US	22/06/1991	Active matrix display screen with storage capacitors formed on conductive lines, semiconductive material, nonconductive material and capacitive lines / Morin, LoComelle
792,262	US	15/11/1991	Method for creating integrated circuit layers of a fixed depth and corresponding integrated circuit / Hiron
9,403,055/6	DE	14/11/1991	
90,143,061	FR	16/11/1990	
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9,403,055/6	GB	14/11/1991	
764,211	US	23/09/1991	Protective circuit for a control circuit in a liquid crystal display screen / Senn, Lelai, Martel
764,196	US	23/09/1991	Sample and hold circuit for a liquid crystal display screen / Senn, Lelai, Martel
886,248	US	18/02/1992	Collection of a monolayer transistor compatible with MOS technology / Moullhat, Boiss
9242004/70	FR	12/02/1992	
91,019,84	FR	12/02/1991	
832,078	US	06/02/1992	Process for forming a localized oxidation on a silicon substrate and corresponding integrated circuit / Stapon, Bang, Muller, Boiss
91,019,81	FR	07/02/1991	
866,342	US	10/04/1992	Process for the production of thin film transistors / Cheouan
91,061,8	US	08/07/1992	Method for manufacturing a vertical folded thin transistor / Cheouan, Boiss, Naudin
92401909/4	DE	08/07/1992	
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91,086,77	FR	10/07/1991	
91,019,13	US	09/07/1992	Active matrix high definition liquid crystal display structure / Morin, Cheouan, Vionze
92401987/0	GB	09/07/1992	
92,042,36	FR	09/07/1992	
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2311024	US	20/04/1994	Method of clock signal recovery and of synchronization for the reception of information elements transmitted by an ATM network and device for the implementation of the method / Rainard
94400853.1	DE	19/04/1994	
93104775	FR	22/04/1993	
94400853.1	GB	19/04/1994	
2281479	US	15/04/1994	Signal processing device using several different filtering, especially for audio frequency coding of voice signals / Balestro, Seim
94400780.6	DE	11/04/1994	
93104552.7	FR	16/04/1993	
94400780.6	GB	11/04/1994	
279546	US	25/07/1994	Optical amplifier having a doped fluoride glass optical fibre and process for producing the amplifier / Ronard, Guilbert, Hamel
94401706.0	GB	25/07/1994	
93109165	FR	26/07/1993	
94401706.0	DE	25/07/1994	
94401706.0	IT	25/07/1994	
299663	US	02/09/1994	Process for the transposition of an optical modulation of one wavelength to another adjustable wavelength / Dupont, Arnaud, Tromp
94401984.3	GB	07/09/1994	
94401984.3	DE	07/09/1994	
9310737	FR	09/09/1993	
385953	US	09/02/1995	Decreasing device, particularly for optical fibers / Crend, Gallean, Gaudelien
94401673	FR	11/02/1994	
95400232.5	DE	03/02/1995	
95400232.5	GB	03/02/1995	
95400232.5	IT	03/02/1995	
377472	US	24/01/1995	Optical amplifier with a doped fluoride glass or optical fibre and process for producing said amplifier / Semelkoff, Ronard, Guilbert
492591	US	20/06/1995	Electron memory addressing device especially for a memory organized into banks / Quilleyer, Dufal
95401444.5	DE	20/06/1995	
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5641120	US	30/03/1995	Electrically conductive polymer compositions, production process and coated substrates / (Clarisse, Delabouglise, Ciprilli)
959152807	GB	30/03/1995	
959152807	DE	30/03/1995	
94108312	FR	31/03/1994	
5271137	US	12/09/1995	Sequential access asynchronous memory device and corresponding process for storage and reading / (Majors, Well)
954022133	DE	04/10/1995	
94112170	FR	12/10/1994	
954022133	GB	04/10/1995	
5693119	US	08/12/1995	Electronic component capable, in particular, of performing a division of two numbers to the base 2 / (Durr, Robert)
9415504	FR	22/12/1994	
954023098	DE	13/12/1995	
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6391088	US	24/04/1996	Device for programmable delay of an analog signal and corresponding acoustic antenna / (Le Rouneur, Balsaro)
9640035110	FR EP	22/04/1996	
9640035110	DE	22/04/1996	
9640035110	GB	22/04/1996	
951049118	FR	25/04/1995	
7084084620	US	21/02/1996	Minimizing program code storage for performing regular and repetitive operations in a programmable processor / (Dutil, Ruyat)
964003339	DE	19/02/1996	
951021311	FR	28/02/1995	
964003339	GB	19/02/1996	
6744675	US	02/07/1996	Apparatus and a method for identifying and splitting multimode fibers / (Le Nonne, Kerdin, Le Marec)
961409814	GB	04/07/1996	
95108042	FR	04/07/1995	
09/462716	US	08/07/1998	Method of minimising the corner effect by densifying the insulative layer / (Schlayonne, Gallard)
2000502539	JP	08/07/1998	
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<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Page 4 of 7</u> <u>Title / Inventors</u> <u>(US Patent)</u>
09/1024849	US	23/06/1998	Process for obtaining a transistor having a silicon-germanium gate / Skene
984014324	EP	12/06/1998	
97079938	FR	25/06/1997	
978749	JP	25/06/1998	
9940827	US	15/06/1998	High density and high capacity distribution frame to optical fibers / Canence, Blandhard, Guignon
984014100	EP	11/06/1998	
9707892	FR	20/06/1997	
09/384362	US	26/08/1999	Instrument for measuring the near end cross talk of a length of multi-core fibers / Boller, Pizapallava
994020102	EP	25/08/1999	
9810768	FR	27/08/1998	
09/289667	US	12/04/1999	Electronic method of signal processing, device for implementing the latter and use thereof
994008522	EP	08/04/1999	
04136509	JP	12/04/1999	
98104557	FR	10/04/1998	
09/956487	US	07/03/2000	Process for testing integrated circuits with access to the memory points of the circuit / Barthe
9902822	FR	03/03/1999	
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009094400	GB	07/03/2000	
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09/979283	US	18/05/2000	Complex number multiplier / Monte, Vo, Anadi
99106425	FR	20/05/1999	
009296260	GB	18/05/2000	
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09/511330	US	23/02/2000	High speed addressable reception device and corresponding multimedia receiver / Andre, Senn
99103769	FR	23/03/1999	
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09/540188	US	31/03/2000	Process for fabricating a planar heterostructure / Hernandez, Campidelli, Rivote, Bensafel
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09/553,916	US	20/04/2000	Integrated circuit device comprising an inductor with high quality coefficient / Merokell, Rons, Senn, Inoumaki
99/04986	FR	20/04/1999	
004010864	EP	19/04/2000	
99/0588	JP	19/04/2000	
10/019,170	US	23/06/2000	Method for compensating non-linearity on a sigma-delta analog-to-digital converter / Morche
99/08323	FR	29/06/1999	
09/518,244	US	06/03/2000	Radio frequency transmitter with a high degree of integration and possibly with self-calibrating image rejection / Andre
004600193	EP	02/03/2000	
99/05768	FR	23/03/1999	
09/569,524	US	28/04/2000	Frequency control system that stabilizes an output through both a dominant voltage controlled oscillator via sampling a generated clock into four states / Majors
004011003	EP	19/04/2000	
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09/505,448	US	16/02/2000	Process for the anisotropic etching of an organic dielectric polymer material by an plasma and application in microelectronics / Glaser, Ruard
99/01925	FR	17/02/1999	
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004003638	EP	09/02/2000	
10/018,179	US	05/06/2000	Semiconductor device with compensated threshold voltage and method for making the same / Skolnik, Cwozdek
00938869	EP	05/06/2000	
99/07491	FR	11/06/1999	
09/980,927	US	26/05/2000	Band-pass filter with carrier frequency reduction / Morche
009369521	GB	26/05/2000	
009369521	JP	26/05/2000	
99/06710	FR	27/05/1999	
10/018,680	US	08/06/2000	Method for making a silicon substrate comprising a buried thin silicon oxide film / Iurozak, Skolnik
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Filing Number	Country Code	Filing Date	Title / Inventors (US Patent)
09/620/896	US	21/07/2000	Method of correcting topographical defects on a micro-electronic substrate // Seifritz, Rüdiger, Schiavone, Rolf
21/9/525	JP	19/07/2000	
2000/0042258	KR	22/07/2000	
99/09/521	FR	22/07/1999	
00402063.3	GB	19/07/2000	
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10/168/041	US	08/06/2001	Low-noise spectroscopic ellipsometer // Remy, J.
00/07426	FR	09/06/2000	
01/94581/0	EP	08/06/2001	
2002/502/401	JP	08/06/2001	
09/912/057	US	25/07/2001	Resin a double resist layer for extreme ultraviolet (EUV) lithography and an extreme ultraviolet light (EUV) lithography process // Seifritz, R.
01/401/972.3	EP	23/07/2001	
00/109/597	FR	26/07/2000	
10/380/756	US	18/09/2001	Device for partial measurement of a radiation frequency // Baryle, Geoffroy
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10/392/582	US	10/07/2001	Samplable absorption structure, in particular for regenerating optical components and method for making same // Maréchal, François, La Courte, Deliaese
00/09080	FR	11/07/2000	
01/964/066.7	EP	10/07/2001	
2002/5098241	JP	10/07/2001	
2/416/127.1	CA	10/07/2001	
01/03/829	FR	21/03/2001	Method and device for transmuting a video sequence, comprising a face, especially in a mobile videophone system // Roux, René
02/16894/7	EP	14/03/2002	

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107251978	US	24/05/2002	Large capacity automatic distribution method for connecting fibres (lay behind method for automatic connection/ disconnection of the binding fibres) (Morelles, Jean-François Bouquet)
02106177	FR	29/03/2001	
01107032	FR	29/03/2001	
021471873	FR	24/05/2002	
20031600628	JP	24/05/2002	

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on December 3rd, 2004

ASSIGNOR

By: France Telecom

Name: François JAMET

Title: Directeur de la Propriété Industrielle et de la Valorisation

(Signature MUST be notarized)

Je soussigné Eric de La HAYE SAINT HILAIRE
Notaire à Paris, Certifie véritable
la signature de M. François JAMET
apposée ci-contre
Paris, le 3 décembre 2004

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